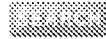




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Server consolidation, an application form of virtualization technology, consolidates mul servers into a single or fewer real machines. It results in higher resource utilization and consumption and is considered as a tendency ...

Keywords: benchmark, dynamic performance, server consolidation, virtual machines**2** [Processor virtualization for secure mobile terminals](#) [Hiroaki Inoue, Junji Sakai, Masato Eda](#)July 2008 **Transactions on Design Automation of Electronic Systems (TODAES)****Publisher:** ACMFull text available: [Pdf](#) (1.53 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index to](#)**Bibliometrics:** Downloads (6 Weeks): 39, Downloads (12 Months): 205, Citation Count: 0

We propose a processor virtualization architecture, VIRTUS, to provide a dedicated dor preinstalled applications and virtualized domains for downloaded native applications. V oriented next-generation mobile terminals can provide ...

Keywords: Multiprocessor, processor virtualization**3** [PinOS: a programmable framework for whole-system dynamic instrumentation](#) [Prashanth P. Bungale, Chi-Keung Luk](#)June 2007 **VEE '07**: Proceedings of the 3rd international conference on Virtual execution**Publisher:** ACMFull text available: [Pdf](#) (399.17 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index to](#)**Bibliometrics:** Downloads (6 Weeks): 20, Downloads (12 Months): 113, Citation Count: 3

PinOS is an extension of the *Pin* dynamic instrumentation framework for whole-system instrumentation, i.e., to instrument both kernel and user-level code. It achieves this by between the subject system and hardware using virtualization ...


Keywords: binary translation, dynamic instrumentation, program analysis tools, virtu whole-system**ADVANCED SEARCH** [Advanced Search](#)**FEEDBACK** [Please provide us with feedback](#)Found **820** of **245,263**

Power-aware dynamic placement of HPC applications

Akshat Verma, Puneet Ahuja, Anindya Neogi

June 2008 **ICS '08**: Proceedings of the 22nd annual international conference on Supercomputing

Publisher: ACM


Full text available:  Pdf (402.46 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index to](#)

Bibliometrics: Downloads (6 Weeks): 55, Downloads (12 Months): 220, Citation Count: 0

High Performance Computing applications and platforms have been typically designed to power consumption. With increased awareness of energy cost, power management even for compute-intensive server clusters. In this work, ...


Keywords: high performance, placement, power management

5 QoS policies and architecture for cache/memory in CMP platforms

 Ravi Iyer, Li Zhao, Fei Guo, Ramesh Illikkal, Srihari Makineni, Don Newell, Yan Solihin, Lis Reinhardt

June 2007 **SIGMETRICS '07**: Proceedings of the 2007 ACM SIGMETRICS international conference on Measurement and modeling of computer systems

Publisher: ACM

Full text available:  Pdf (459.91 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 32, Downloads (12 Months): 307, Citation Count: 6

As we enter the era of CMP platforms with multiple threads/cores on the die, the diverse simultaneous workloads running on them is expected to increase. The rapid deployment of virtualization as a means to consolidate workloads on to a single ...

Keywords: CMP, QoS, cache/memory, performance, quality of service, resource sharing, service level agreements

Also published in:


June 2007 **SIGMETRICS Performance Evaluation Review** Volume 35 Issue 1

6 VIRTUS: a new processor virtualization architecture for security-oriented next-generation terminals

 Hiroaki Inoue, Akihisa Ikeno, Masaki Kondo, Junji Sakai, Masato Eda

July 2006 **DAC '06**: Proceedings of the 43rd annual conference on Design automation

Publisher: ACM

Full text available:  Pdf (798.11 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index to](#)

Bibliometrics: Downloads (6 Weeks): 5, Downloads (12 Months): 101, Citation Count: 2

We propose a new processor virtualization architecture, VIRTUS, to provide a dedicated pre-installed applications and virtualized domains for downloaded native applications. In security-oriented next-generation mobile terminals can provide ...


Keywords: multiprocessor, processor virtualization

7 Dynamic security domain scaling on symmetric multiprocessors for future high-end systems

 Hiroaki Inoue, Akihisa Ikeno, Tsuyoshi Abe, Junji Sakai, Masato Eda

September 2007 **CODES+ ISSS '07**: Proceedings of the 5th IEEE/ACM international conference on Hardware/software codesign and system synthesis

Publisher: ACM

Full text available:  Pdf (450.62 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index to](#)

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 95, Citation Count: 1

We propose a method for dynamic security domain scaling on SMPs that offers both high performance and high security for future high-end embedded systems. Its most important feature is its highly efficient use of processor resources, accomplished ...

Keywords: AMP, SMP, dynamic security domain scaling

8 Performance evaluation of adaptive MPI

 Chao Huang, Gengbin Zheng, Laxmikant Kalé, Sameer Kumar

March 2006 **PPoPP '06:** Proceedings of the eleventh ACM SIGPLAN symposium on Principles and Practice of Parallel Programming

Publisher: ACM

Full text available:  Pdf (399.56 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [cited by](#)

Bibliometrics: Downloads (6 Weeks): 12, Downloads (12 Months): 105, Citation Count: 3

Processor virtualization via migratable objects is a powerful technique that enables the system to carry out intelligent adaptive optimizations like dynamic resource management. This paper describes an early language/system that supports migratable ...

Keywords: MPI, adaptivity, communication optimization, load balancing, processor virtualization

9 Time Jails: A Hybrid Approach to Scalable Network Emulation

Andreas Grau, Steffen Maier, Klaus Herrmann, Kurt Rothermel

June 2008 **PADS '08:** Proceedings of the 22nd Workshop on Principles of Advanced and Distributed Simulation

Publisher: IEEE Computer Society

Full text available:  Pdf (274.39 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 26, Citation Count: 0

It is essential to evaluate the performance of newly developed distributed software and protocols. Network emulation enables reproducible evaluation of unmodified real implementations. This paper describes a software built for distributed systems, such as a large scale ...

10 Network virtualization under user control

Omar Cherkaoui, Elbiaze Halima

March 2008 **International Journal of Network Management**, Volume 18 Issue 2

Publisher: John Wiley & Sons, Inc.


Full text available:  Pdf (483.14 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 198, Citation Count: 0

In this paper, we present the main challenges for offering end-to-end virtual network service under user control. This work is motivated by the growing need for end-to-end virtual service for high-volume data transfer applications such as large ...

11 A parallel dynamic compiler for CIL bytecode

 Simone Campanoni, Giovanni Agosta, Stefano Crespi Reghizzi

April 2008 **SIGPLAN Notices**, Volume 43 Issue 4

Publisher: ACM

Full text available:  Pdf (851.21 KB)

Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 91, Citation Count: 0

Multi-core technology is being employed in most recent high-performance architecture designs. These architectures need specifically designed multi-threaded software to exploit all the potential of the hardware. This paper describes a multi-threaded software that exploits the potential of the hardware to improve the performance of multi-threaded applications. ...

their hardware parallelism. At the same time, object code ...



Keywords: dynamic compilation, parallel virtual machine, virtual execution system

12 [Xen and the Art of Cluster Scheduling](#)

[Niels Fallenberg](#), [Hans-Joachim Picht](#), [Matthew Smith](#), [Bernd Freisleben](#)

November 2006 **VTDC '06: Proceedings of the 2nd International Workshop on Virtualization in Distributed Computing**

Publisher: IEEE Computer Society

Full text available:  [Publisher Site](#),  [Pdf](#) (7.23 MB) Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): 10, Downloads (12 Months): 148, Citation Count: 2

In shared use clusters, scheduling systems must schedule both serial and parallel jobs manner, while at the same time optimizing overall cluster efficiency. Since serial and p conflict considerably, scheduling both types of jobs ...


13 [Image management in a virtualized data center](#)



[Tingxi Tan](#), [Rob Simmonds](#), [Bradley Arlt](#), [Martin Arlitt](#), [Bruce Walker](#)

August 2008 **SIGMETRICS Performance Evaluation Review**, Volume 36 Issue 2

Publisher: ACM

Full text available:  [Pdf](#) (305.23 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index te](#)

Bibliometrics: Downloads (6 Weeks): 20, Downloads (12 Months): 48, Citation Count: 0

Industrial research firms such as Gartner and IDC are predicting an explosion in the nu services in the coming years. Virtualization technologies could play an important role in as they create an opportunity to provide services ...

Keywords: image management, performance, scalability, virtualization


14 [Protectit: trusted distributed services operating on sensitive data](#)



[Jiantao Kong](#), [Karsten Schwan](#), [Min Lee](#), [Mustaque Ahamad](#)

April 2008 **Eurosys '08: Proceedings of the 3rd ACM SIGOPS/EuroSys European Conference on Computer Systems 2008**

Publisher: ACM

Full text available:  [Pdf](#) (482.44 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index te](#)

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 231, Citation Count: 0

Protecting shared sensitive information is a key requirement for today's distributed ap research uses virtualization technologies to create and maintain trusted data paths ac machines, for the services being run and their ...

Keywords: data protection, traffic interception, trusted data path

Also published in:

April 2008 **SIGOPS Operating Systems Review** Volume 42 Issue 4

15 [Live data center migration across WANs: a robust cooperative context aware appr](#)



[K. K. Ramakrishnan](#), [Prashant Shenoy](#), [Jacobus Van der Merwe](#)

August 2007 **INM '07: Proceedings of the 2007 SIGCOMM workshop on Internet network i**

Publisher: ACM


Full text available:  [Pdf](#) (202.23 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index te](#)

Bibliometrics: Downloads (6 Weeks): 16, Downloads (12 Months): 145, Citation Count: 0

A significant concern for Internet-based service providers is the continued operation of services in the face of outages, whether planned or unplanned. In this paper we advocate a cooperative, context-aware approach to data center migration ...


Keywords: data center migration, storage, virtual server

16 Remote specialization for efficient embedded operating systems

 Sapan Bhatia, Charles Consel, Calton Pu

July 2008 **Transactions on Programming Languages and Systems (TOPLAS)**, Volume 29, Number 4

Publisher: ACM


Full text available:  Pdf (885.48 KB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 24, Downloads (12 Months): 164, Citation Count: 0

Prior to their deployment on an embedded system, operating systems are commonly tailored to reduce code size and improve runtime performance. Program specialization is a promising approach to this process: it is predictable and modular, and it allows the ...

Keywords: Remote specialization, compilers, performance analysis, specialization server

17 Proceedings of the 3rd international workshop on Middleware for grid computing

 Radha Nandkumar, Bruno Schulze, Peter Henderson

November 2005 **MGC '05: Proceedings of the 3rd international workshop on Middleware for grid computing**


Publisher: ACM

Additional Information: [full citation](#), [abstract](#)

Bibliometrics: Downloads (6 Weeks): n/a, Downloads (12 Months): n/a, Citation Count: 0

It is a great pleasure to introduce the ACM/IEEE/IFIP Middleware Workshop Program for 2005. This is the fourth time that Middleware has included a Workshop Program, and the interest and the standard of submissions continue to be ...

18 Delay tolerant network simulation with VNUML

 Lidice Romero Amondaray, Joaquín Seoane Pascual

September 2008 **CHANTS '08: Proceedings of the third ACM workshop on Challenged network architectures**

Publisher: ACM


Full text available:  Pdf (1.02 MB) Additional Information: [full citation](#), [abstract](#), [references](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 8, Downloads (12 Months): 65, Citation Count: 0

This paper describes a simulation environment to easily create complex Delay Tolerant Network (DTN) scenarios using Virtual Network User Mode Linux (VNUML), a network virtualization environment based in User Mode Linux virtual machines running the DTN ...

Keywords: delay tolerant networks, simulation, visualization tool

19 Chip-level integration: the new frontier for microprocessor architecture

 Jaime H. Moreno

July 2006 **SPAA '06: Proceedings of the eighteenth annual ACM symposium on Parallelism in algorithms and architectures**

Publisher: ACM

Full text available:  Pdf (154.63 KB) Additional Information: [full citation](#), [abstract](#), [index terms](#)


Bibliometrics: Downloads (6 Weeks): 7, Downloads (12 Months): 52, Citation Count: 0

Technology trends in recent years have led to the transition from chips containing one core to chips containing multiple cores. This transition, first illustrated in server system

Power4 microprocessor, is currently visible ...

Keywords: chip-level integration, microprocessor architecture

20 [Charm++ simplifies coding for the cell processor](#)

 [David M. Kunzman, Gengbin Zheng, Eric Bohm, James C. Phillips, Laxmikant V. Kale](#)
November 2006 **SC '06:** Proceedings of the 2006 ACM/IEEE conference on Supercomputing
Publisher: ACM

Full text available:  [Html](#) (2.17 KB)

Additional Information: [full citation](#), [abstract](#), [index terms](#)

Bibliometrics: Downloads (6 Weeks): 6, Downloads (12 Months): 22, Citation Count: 1

While the Cell processor, jointly developed by IBM, Sony, and Toshiba, has great comp power, it also presents many challenges including portability and ease of programming been adapting the Charm++ Runtime System to utilize the Cell. ...

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